

Introduction

The CL11011P650 is a sub-LVDS Drivers optimized for high data rate and low power applications. The CL11011P650 is designed to support data rates in excess of 650Mbps utilizing sub Low Voltage Differential Signaling (sub-LVDS) technology. The differential driver outputs provide low EMI with its typical low output swing of 150mV.

Feature

- Max 650Mbps switching rate
- Low power single 2.8 / 3.3V (Option: 1.0 / 1.2 / 1.8V Logic/Level Shifter)
- SMIA CCP Class 0-2 and MIPI CSI compliant
- 150mV swing sub-LVDS for low EMI
- Power Down Mode ~1uA

Block Diagram

