

Introduction

The CL11012IP650 is sub-LVDS ReceiverAMP optimized for high data rate and low power applications. The CL11012IP650 is designed to support data rates in excess of 650Mbps utilizing sub Low Voltage Differential Signaling (sub-LVDS) technology. The CL11012IP650 accept low voltage (150mV typical) differential input signals and translates them to CMOS output levels.

Feature

- Max 650Mbps switching rates
- Low power single 2.8 / 3.3V (Option: 1.0 / 1.2 / 1.8V Logic/Level Shifter)
- SMIA CCP Class 0-2 and MIPI CSI compliant
- Supports Fail-Safe function to all input channels
- Power Down Mode ~1uA

Block Diagram

