

Introduction

The CL11061IP1000 is an ideal means to link mobile camera modules to baseband processors and baseband processors to LCD panels. The CL11061IP1000 is designed to support data rate in excess of 680M/1Gbps utilizing sub-LVDS and MIPI D-PHY 1.00 specification for Mode pin. The differential outputs provide low EMI with its typical low output swing of 150/200mV.

Feature

- Maximum output data rate: ~1Gbps (sub-LVDS: ~680Mbps)
- SMIA CCP Class-0, 1, 2 / MIPI-DPHY Ver.1.00.00 compliant
- Low power single 1.2v (Option: 1.8V) (Option : 1.0 / 1.2V Logic/Level Shifter)
- 150/200mV swing for low EMI

Block Diagram

