

Introduction

The CL11063IP1000 is an ideal means to link mobile camera modules to baseband processors and baseband processors to LCD panels. The CL11063IP1000 is designed to support data rate in excess of 680M/1Gbps utilizing sub-LVDS and MIPI D-PHY 1.00 specification for Mode pin.

Feature

- Maximum output data rate: ~1Gbps (sub-LVDS: ~680Mbps)
- SMIA CCP Class-0, 1, 2 / MIPI-DPHY Ver.1.00.00 compliant
- Low power 1.2/1.8v (Option: 1.8V single) (Option : 1.0 / 1.2V Logic/Level Shifter)
- 150/200mV swing for low EMI

Block Diagram

