

Introduction

The CL11071IP1000 is an ideal means to link mobile camera modules to baseband processors and baseband processors to LCD panels. The CL11071IP1000 is designed to support data rate in excess of 680M/1Gbps utilizing MIPI D-PHY 1.00 and MDDI ver.1.2 specification for Mode pin. The differential outputs provide low EMI with its typical low output swing of 200mV.

Feature

- Maximum output data rate: ~1Gbps
- MIPI-DPHY Ver.1.00.00 / MDDI ver.1.2 compliant
- Low power single 1.8v (Option : 1.0 / 1.2V Logic/Level Shifter)
- 200mV swing for low EMI

Block Diagram

