

Introduction

The CL11112IP800 is an ideal means to link mobile camera modules to baseband processors and baseband processors to LCD panels. The CL11112IP800 is designed to support data rate in excess of 800M/680Mbps utilizing SLVS-400 and sub-LVDS specification for Mode pin. The differential outputs provide low EMI with its typical low output swing of 150/200mV.

Feature

- Maximum output data rate: ~800Mbps (sub-LVDS: ~680Mbps)
- JEDEC SLVS-400 / SMIA CCP Class-0, 1, 2 compliant
- Low power single 1.8v (Option : 1.0 / 1.2V Logic/Level Shifter)
- 150/200mV swing for low EMI
- Available for Aptina HiSPi specification

Block Diagram

