

Introduction

The CL12812M8RIP10000 is an ideal means to link Camera Modules or CMOS Image Sensor (CIS) to ISP (Imaging Signal Processor) and DSP. The CL12812M8RIP10000 is designed to support data rate in excess of maximum 10Gbps utilizing SLVS-EC ver.3.0 interface specification.

Feature

- SLVS-EC ver.3.0 compliant
- Supporting for two kind Differential Input Signals
SLVS-EC (Maximum 10.0Gbps)
- Xtal Input Clock Frequency Selectable
 - 25MHz /50MHz / 75MHz @ 10Gbps (5Gbps, 2.5Gbps, 1.250Gbps)
 - 37.125MHz /54MHz @ 9.504Gbps (4.752Gbps, 2.376Gbps, 1.188Gbps)
 - 24MHz /48MHz / 72MHz @ 9.216Gbps (4.608Gbps, 2.304Gbps, 1.152Gbps)
- Maximum Output Clock Frequency
 - ~1000MHz @ Select parallel data bus width to 10bit
 - ~250MHz @ Select parallel data bus width to 40bit
- Power Supply: Vcc=1.8V (IO and Analog) Vdd=0.8 V (Inside Core)
- Maximum Lane Number: 8-Lane
- 40-bit/Lane Parallel Outputs (SLVS-EC)
- Including Power Down Mode
- Including Differential Low Detect Circuit for SLVS-EC
- TSMC 12nm FFC Process 1P11M_2Xa1Xd_h_3Xe_VHV_2Y2R
(Using of Standard Vth and Low Vth and ultra Low Vth Transistor)
- IO Direction: South-North

Support Link controller

- CD12812 Link controller

We can provide process porting and lane customization.
If you have any questions or requirements,
such as a detailed information of Curious IP products,
please contact us at the email address : curious.info@curious-jp.com