

Introduction

The CL12832M8R2JM3KIP2400 is an ideal means to link Camera Modules or CMOS Image Sensor (CIS) to ISP (Imaging Signal Processor) and DSP. The CL12832M8R2JM3KIP2400 is designed to support data rate in excess of maximum 2.4Gbps utilizing SLVS-EC / sub-LVDS / CMOS 1.8V interface specification. The CL12832M8R2JM3KIP2400 can change Interface type to same PAD for changing mode.

Feature

- SLVS-EC ver.1.2 compliant
- Supporting for four kind Differential Input Signals
 - 1) SLVS-EC (Maximum 2.4Gbps)
 - 2) sub-LVDS (Maximum 800Mbps)
 - 3) CMOS 1.8V (Maximum 166MHz)
- Xtal Input Clock Frequency Selectable 24MHz / 37.125MHz / 54MHz / 72MHz
- Maximum Input Clock Frequency ~400MHz (sub-LVDS)
- Maximum Output Clock Frequency ~240MHz (SLVS-EC)
- Power Supply : Vcc=1.8V (IO and Analog) Vdd=0.9 V (Inside Core)
- Maximum Lane Number : 8-Lane
- 10-bit/Lane Parallel Outputs (SLVS-EC)
8-bit/Lane Parallel Outputs (sub-LVDS Serial)
1-bit/Lane Parallel Outputs (sub-LVDS Parallel)
- Including Power Down Mode
- Including "Hi-Z" Detect Circuit for SLVS-EC
- TSMC 28nm HPC Process 1P10M5X2R (Using of Standard Vth Transistor)
- Poly Direction: South-North
- Various process porting support available (Please contact us.)
- Supporting Link-layer: CD12832IP soft macro

If you have any questions or requirements,
such as a detailed information of Curious IP products,
please feel free to contact us at the email address :
curious.info@curious-jp.com